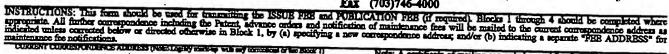


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APPLICATION NO.

FILING DATE

FIRST NAMED INVENTOR

CPAC 1011-2 US

9980

10/081,425 02/22/2002 Rajendra Pendse

TITLE OF INVENTION: APPARATUS AND PROCESS FOR PRECISE ENCAPSULATION OF FLIP CHIP INTERCONNNECTS

APPLN, TYPE	SMALL ENTITY	ISSUE FEE	Will To a Time Time	T	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of Rajendra PENDSE Examiner: Scott B. Geyer Application No.: 10/081,425 Group Art Unit: 2829 Filed: February 22, 2002 Date: May 14, 2003 Title: Apparatus and process for precise encapsulation of flip chip interconnects

SEPARATE LETTER TO OFFICIAL DRAFTSPERSON

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In connection with the above-referenced U. S. patent application, transmitted herewith are the following papers:

One (1) sheet of formal drawing(s) (Figs 1, 2, 3, 4, and 5). [X]

Authorization is hereby given to deduct from or credit to Deposit Account No. 50-0869 (CPAC 1011-2 US), any additional fees that may be required with this matter.

ctfully submitted,

Ru. No. 53,609

Date: May 14, 2003

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